

**/ Descriptions**

TO-92          PNP                          Silicon PNP transistor in a TO-92 Plastic Package.

**/ Features**

        , 2SC1788  
low  $V_{CE(sat)}$ , complementary pair with 2SC1788.

**/ Applications**

Audio frequency output amplifier.

**/ Equivalent Circuit**



**/ Pinning**



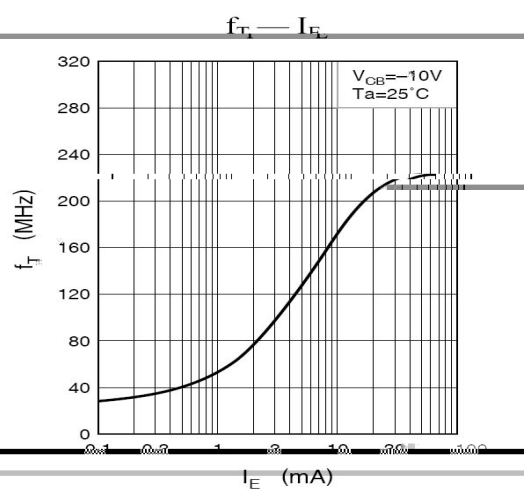
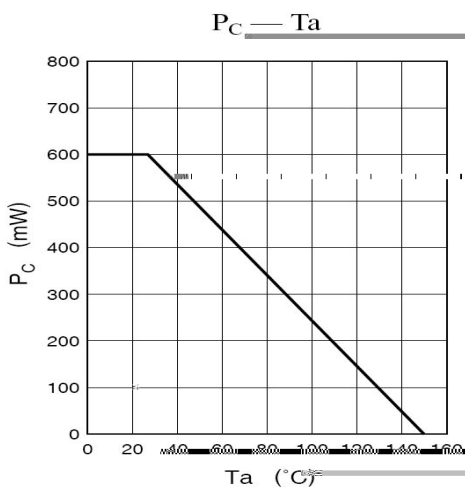
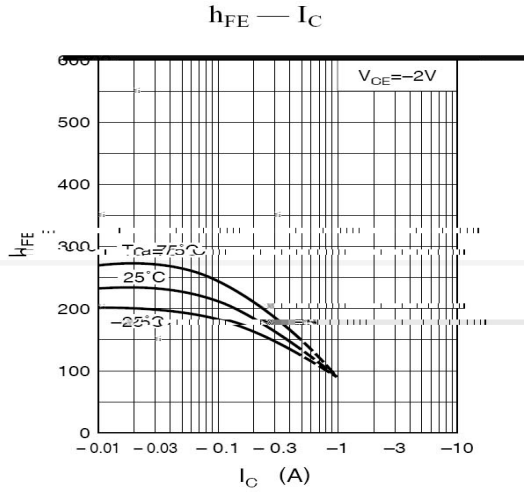
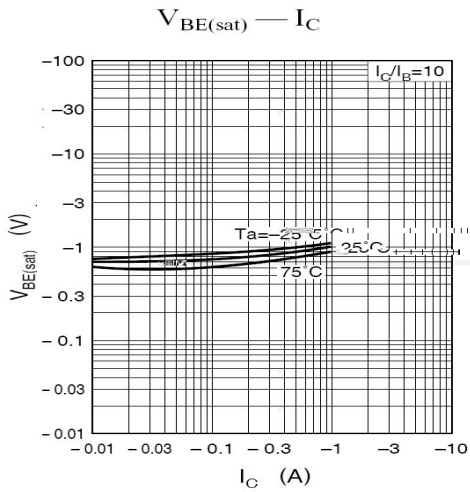
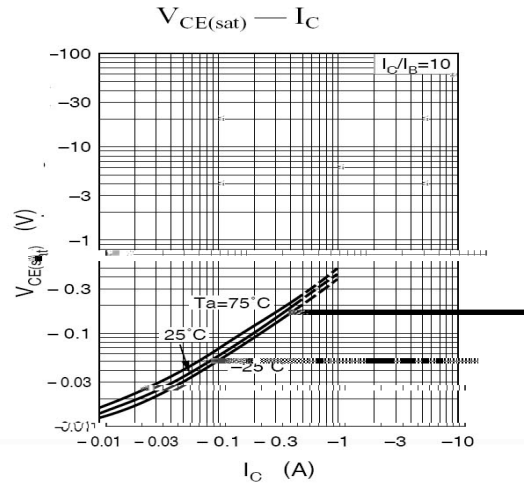
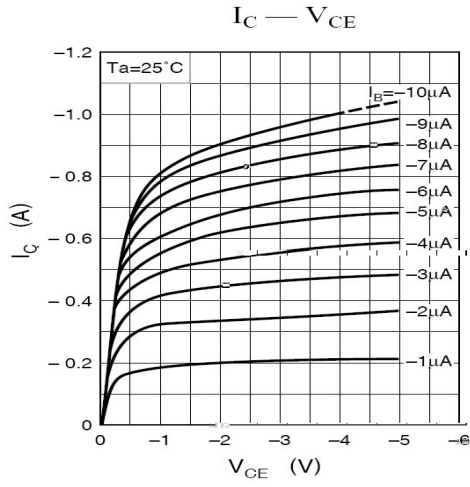
PIN1 Base          PIN 2 Collector          PIN 3 Emitter

**/ hFE Classifications & Marking**

h <sub>FE</sub> Classifications Symbol	Q	R	S
h <sub>FE</sub> Range	90~155	130~220	180~350

Parameter	Symbol	Rating	Unit
Collector to Base Voltage	$V_{CBO}$	-25	V
Collector to Emitter Voltage	$V_{CEO}$	-20	V
Emitter to Base Voltage	$V_{EBO}$	-7.0	V
Collector Current – Continuous(Pulse)	$I_{CP}$	-1.0	A
Collector Current - Continuous	$I_C$	-0.5	A
Collector Power Dissipation	$P_C$	625	mW
Junction Temperature	$T_j$	150	
Storage Temperature Range	T		

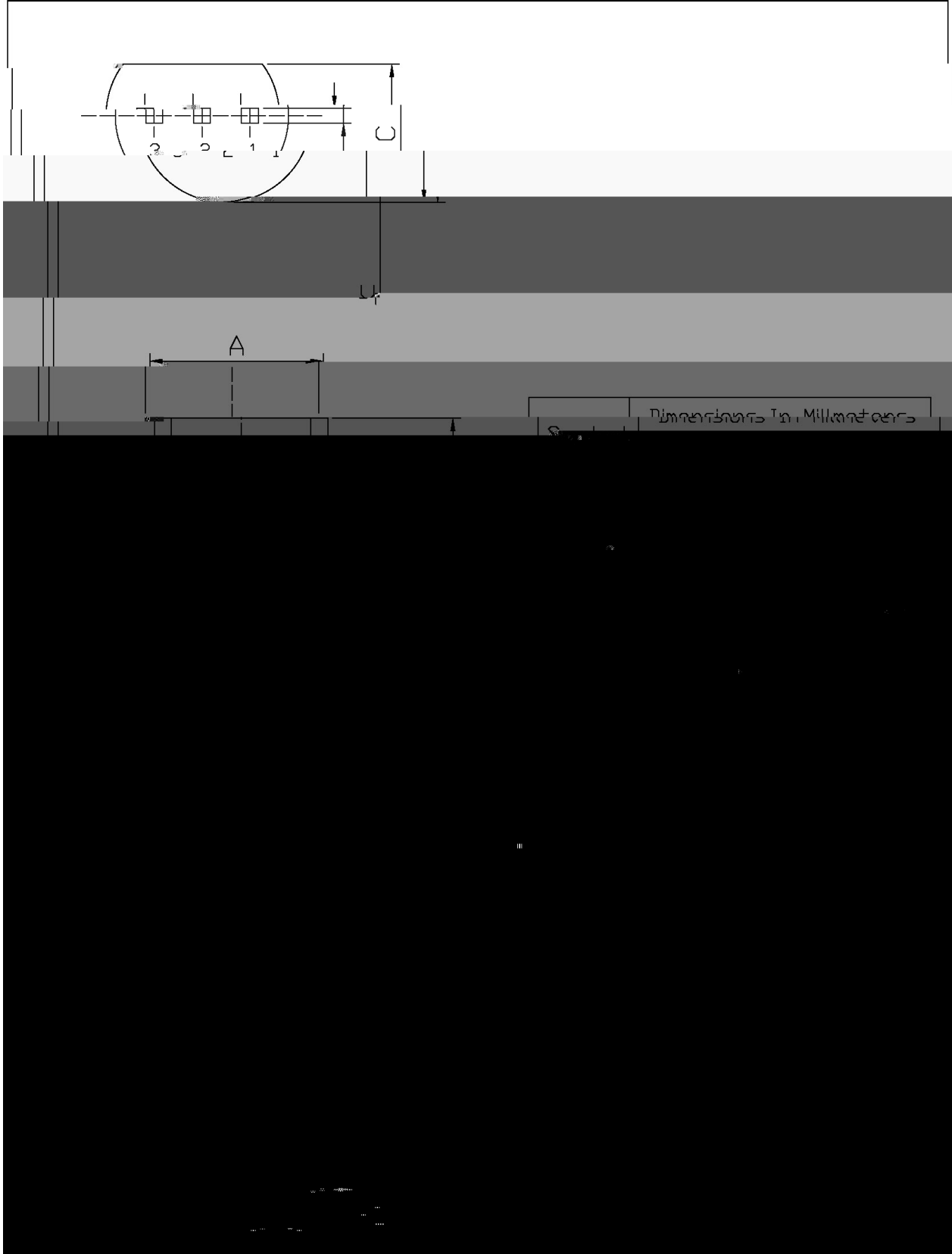
/ Electrical Characteristic Curve



/ Package Dimensions

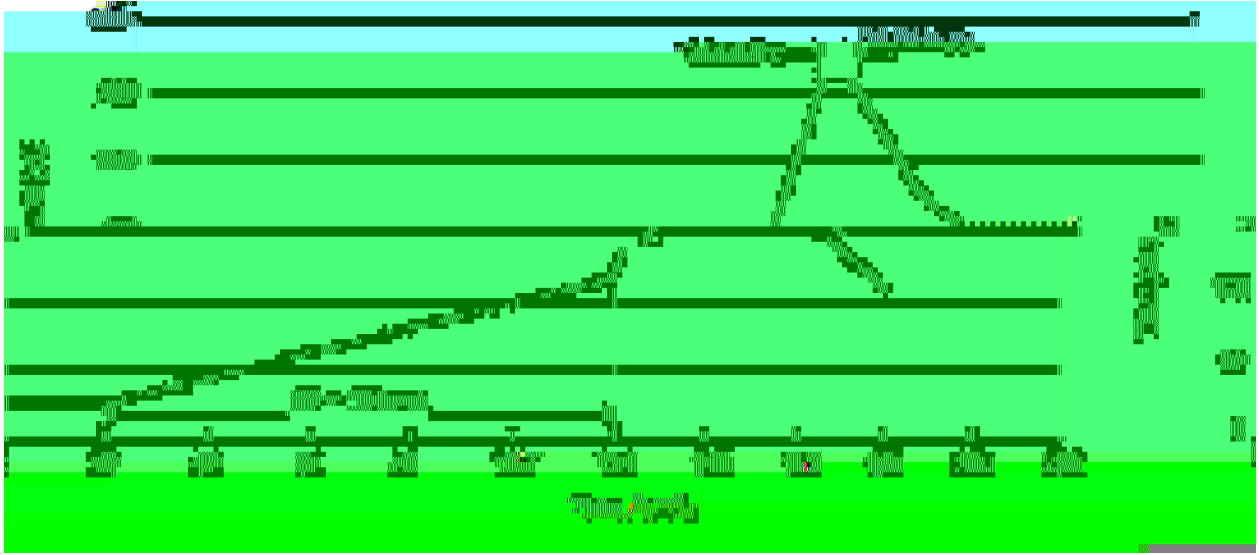
T0-92

Unit: mm





**( ) / Temperature Profile for Dip Soldering(Pb-Free)**



Note:

- |   |       |     |           |   |                                      |
|---|-------|-----|-----------|---|--------------------------------------|
| 1 | 25    | 150 | 60        | 90sec;                                  | 1.Preheating:25~150 , Time:60~90sec. |
| 2 | 255±5 |     | 5±0.5sec; | 2.Peak Temp.:255±5 , Duration:5±0.5sec. |                                      |
| 3 |       | 2   | 10 /sec.  | 3. Cooling Speed: 2~10 /sec.            |                                      |

**/ Resistance to Soldering Heat Test Conditions**

270±5                      10±1 sec.                      Temp:270±5                      Time:10±1 sec

**/ Packaging SPEC.**

/ BULK

Package Type	Units					Dimension (unit mm3)		
	Units/Bag /	Bags/Inner Box /	Units/Inner Box /	Inner Boxes/Outer Box /	Units/Outer Box /	Bag	Inner Box	Outer Box

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