

**/ Descriptions**

Silicon PNP transistor in a TO-18 Plastic Package.

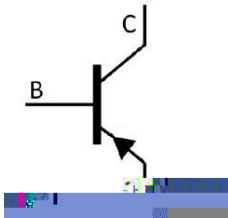
**/ Features**

Complementary pair with 2SC1162.

**/ Applications**

Low frequency power amplifier applications.

**/ Equivalent Circuit**



**/ Pinning**



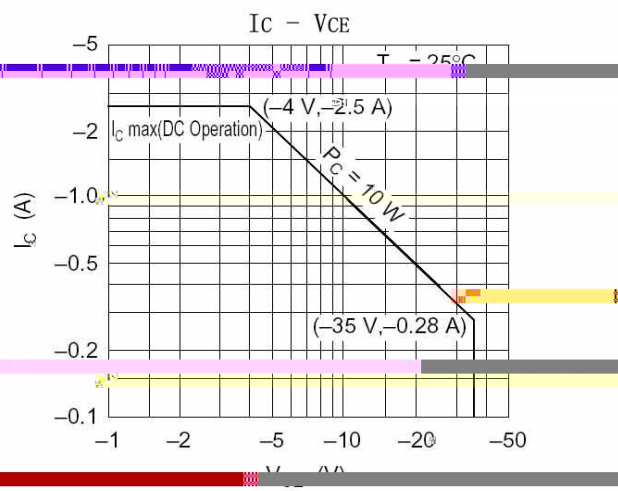
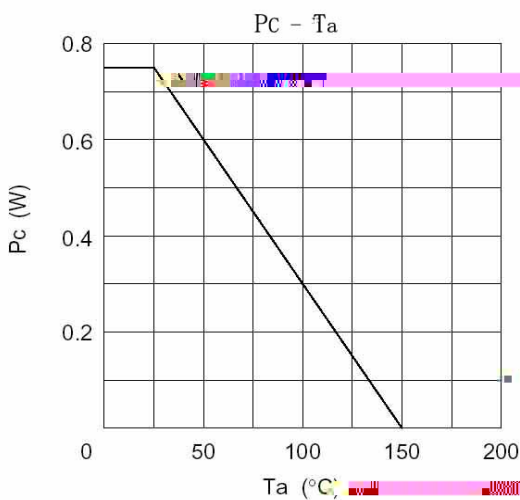
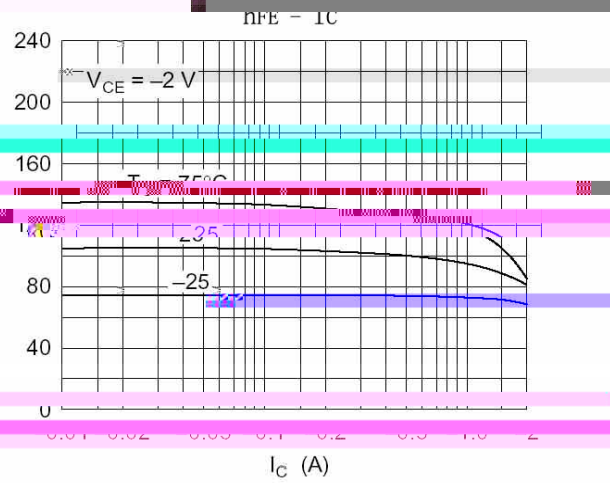
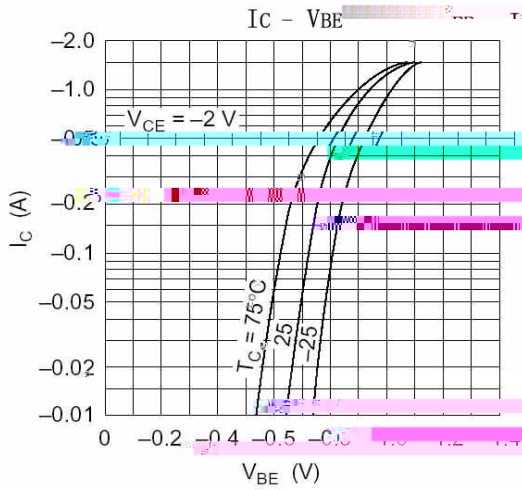
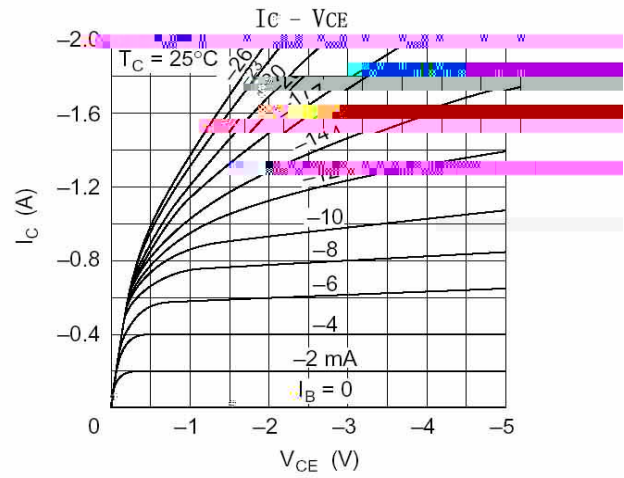
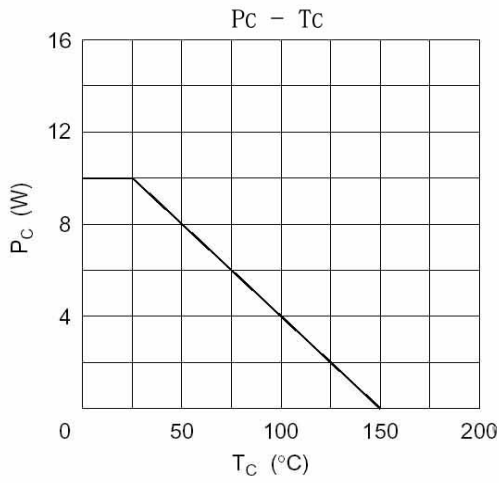
PIN1 Emitter      PIN 2 Collector      PIN 3 Base

**/  $h_{FE}$  Classifications & Marking**

| $h_{FE}$ Classifications<br>Symbol | B      | C       | D       |
|------------------------------------|--------|---------|---------|
| $h_{FE}$ Range                     | 60 120 | 100 200 | 160 320 |

| Parameter                      | Symbol     | Rating | Unit |
|--------------------------------|------------|--------|------|
| Collector to Base Voltage      | $V_{CBO}$  | -35    | V    |
| Collector to Emitter Voltage   | $V_{CEO}$  | -35    | V    |
| Emitter to Base Voltage        | $V_{EBO}$  | -5.0   | V    |
| Collector Current - Continuous | $I_C$      | -2.5   | A    |
| Peak Collector Current         | $I_{CP}$   | -3.0   | A    |
| Collector Power Dissipation    | $P_{CEBO}$ |        |      |

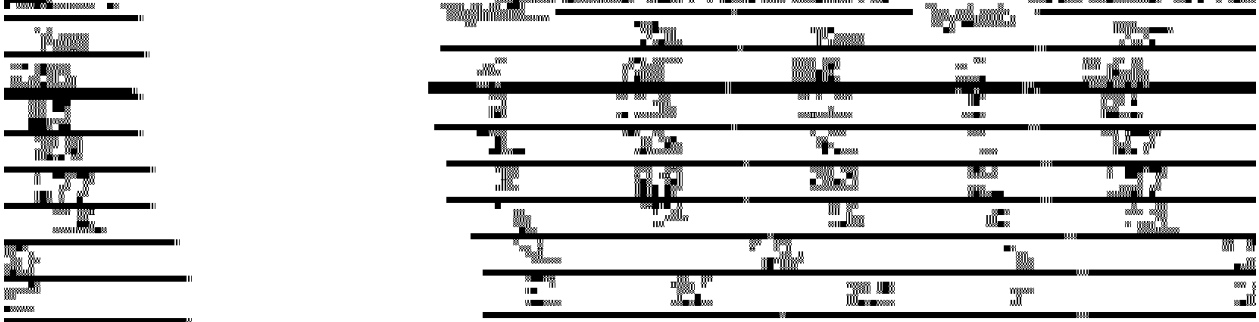
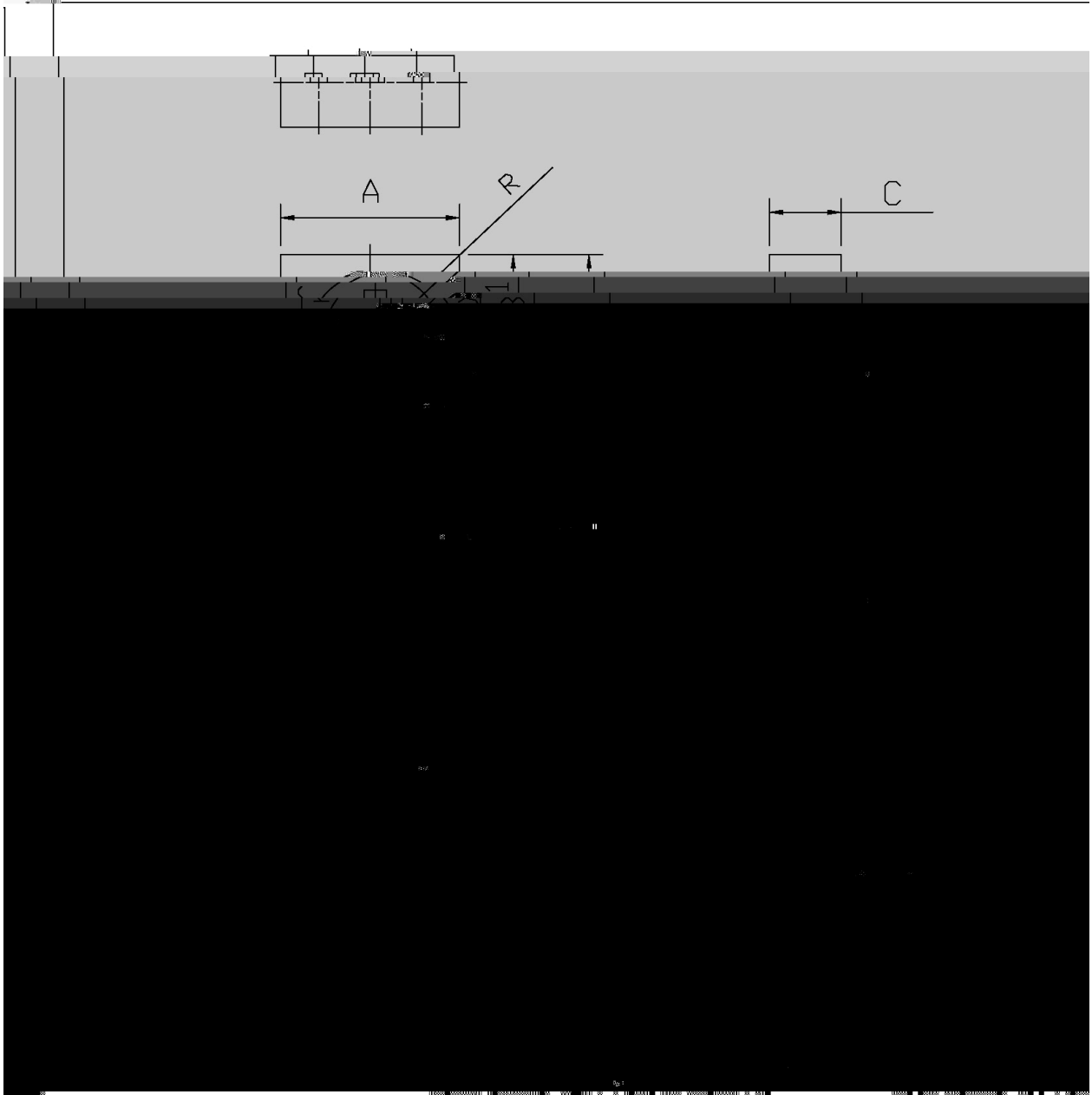
/ Electrical Characteristic Curve



/ Package Dimensions

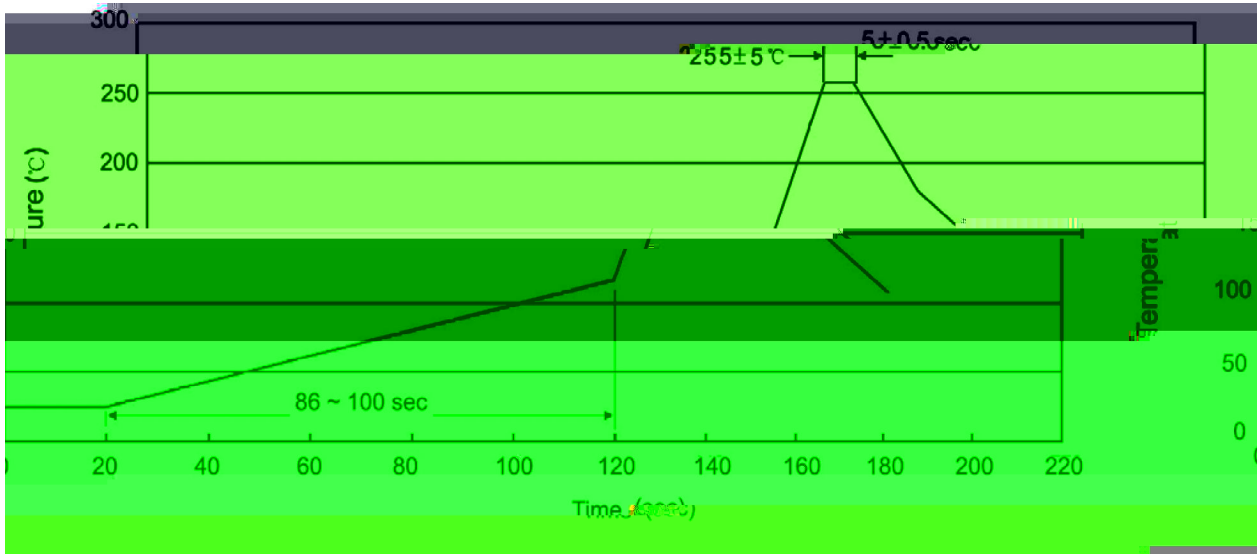
T0-126

单位: mm





( ) / Temperature Profile for Dip Soldering(Pb-Free)



Note:

- 1            25   150            60   90sec;
- 2            255

1.Preheating:25~150 , Time:60~90sec.