





SOT-23          PNP          Silicon PNP transistor in a SOT-23 Plastic Package.

$$V_{CE(sat)}$$

High current gain, Low collector-emitter saturation voltage.

General power amplifier application.



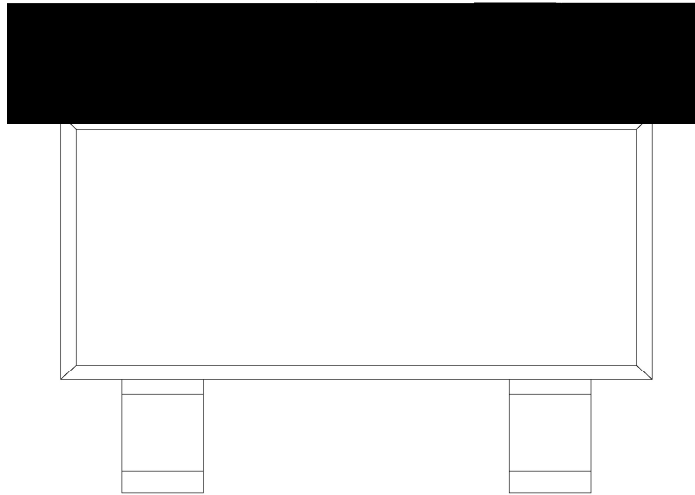
PIN1   Base          PIN 2   Emitter          PIN 3   Collector

$h_{FE}$ Classifications Symbol	A	B	C
$h_{FE}$ Range	125~250	220~475	420~800
Marking	H3J	H3K	H3L



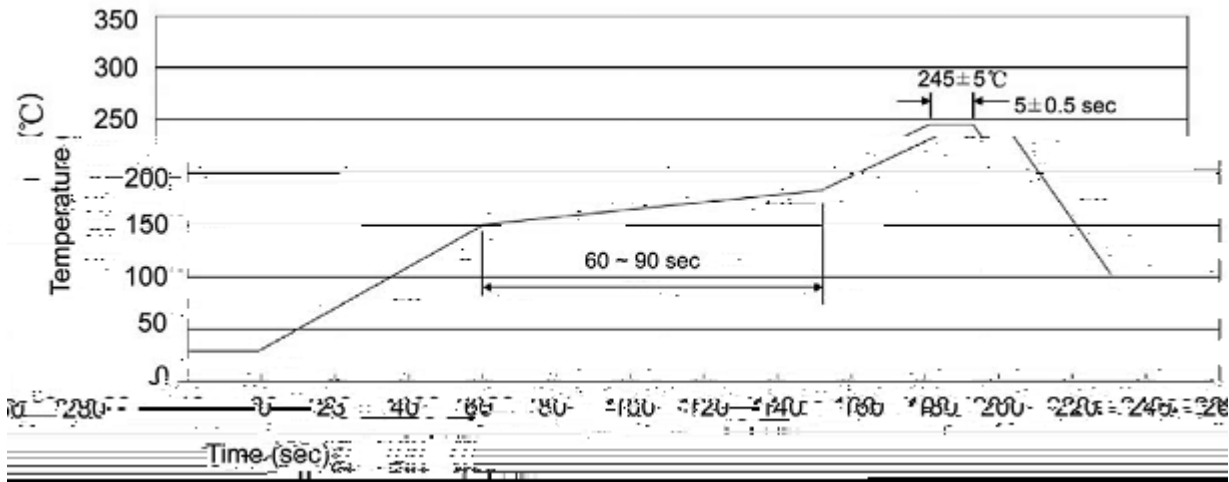
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Temperature Profile for IR Reflow Soldering(Pb-Free)



Note:

- |   |       |     |    |           |   |
|---|-------|-----|----|-----------|---|
| 1 | 25    | 150 | 60 | 90sec;    | 1.Preheating:25~150 , Time:60~90sec.    |
| 2 | 245±5 |     |    | 5±0.5sec; | 2.Peak Temp.:245±5 , Duration:5±0.5sec. |
| 3 |       |     | 2  | 10 /sec.  | 3. Cooling Speed                        |