

# BRCL4070ZR-4.2

Rev.B Mar.-2026



DATA SHEET

BRCL4070ZR-4.2

1.2A

/  
BRCL4070ZR-4.2

P-MOSFET

4.2V

4.2V

1/10

BRCL4070ZR-4.2

6.8V OVP  
36V

0V

1.2A

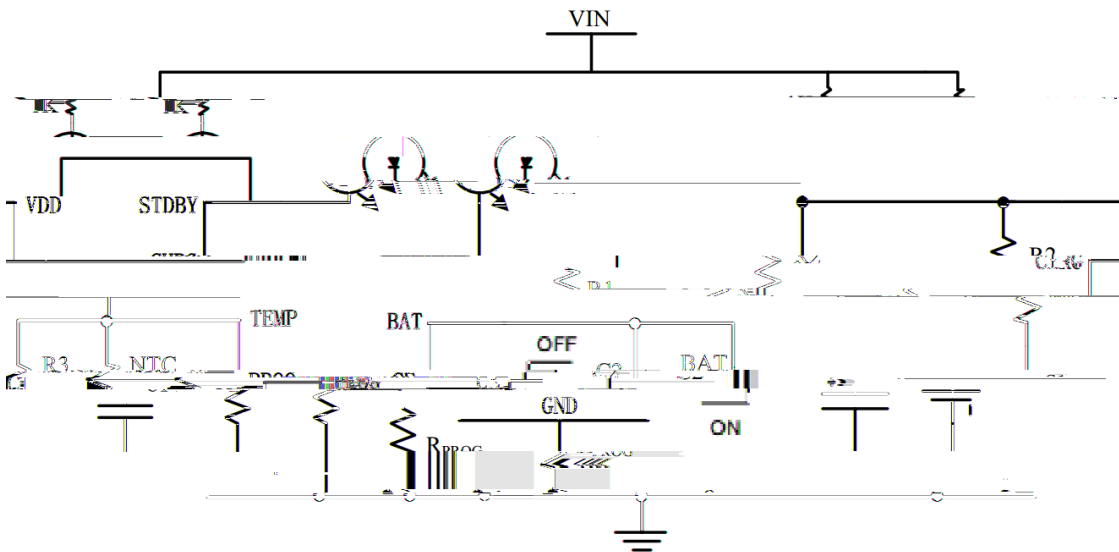
/ /

2.8V

DFN3x3B-8L

RoHS

LJ9

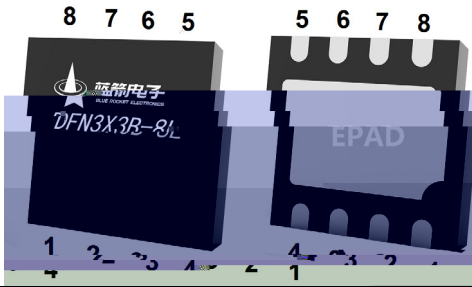


: JD ; (\$' u= I JD ; +%\$( ' I

# BRCL4070ZR-4.2

Rev.B Mar.-2026

## / Pinning



PIN Num.	Symbol	Function
1	TEMP	
2	GI F >	
3	GND	
4	VDD	, M
5	BAT	
6	STDBY	
7	CHRG	
8	CE	
EPAD	GND	

## / Absolute Maximum Ratings(Ta=25 )

PARAMETER	SYMBOL	RATINGS	UNITS
VDD/CHRG/STDBY Pin Voltage	$V_{VDD/CHRG/STDBY}$	-0.3~30	V
VCE Pin Voltage	$V_{CE}$	-0.3~36	
BAT Pin Voltage	$V_{BAT}$	-5~15	
PROG Pin Voltage	$V_{PROG}$	-0.3~6	
TEMP Pin Voltage	$V_{TEMP}$	-0.3~25	
Operating Ambient Temperature Range	$T_{OP}$	-40~+85	°C
Storage Temperature	$T_{stg}$	-55~+150	°C
Lead Temperature (Soldering, 10s)	$T_{solder}$	260	°C
ESD	HBM	2000	V
	MM	200	V

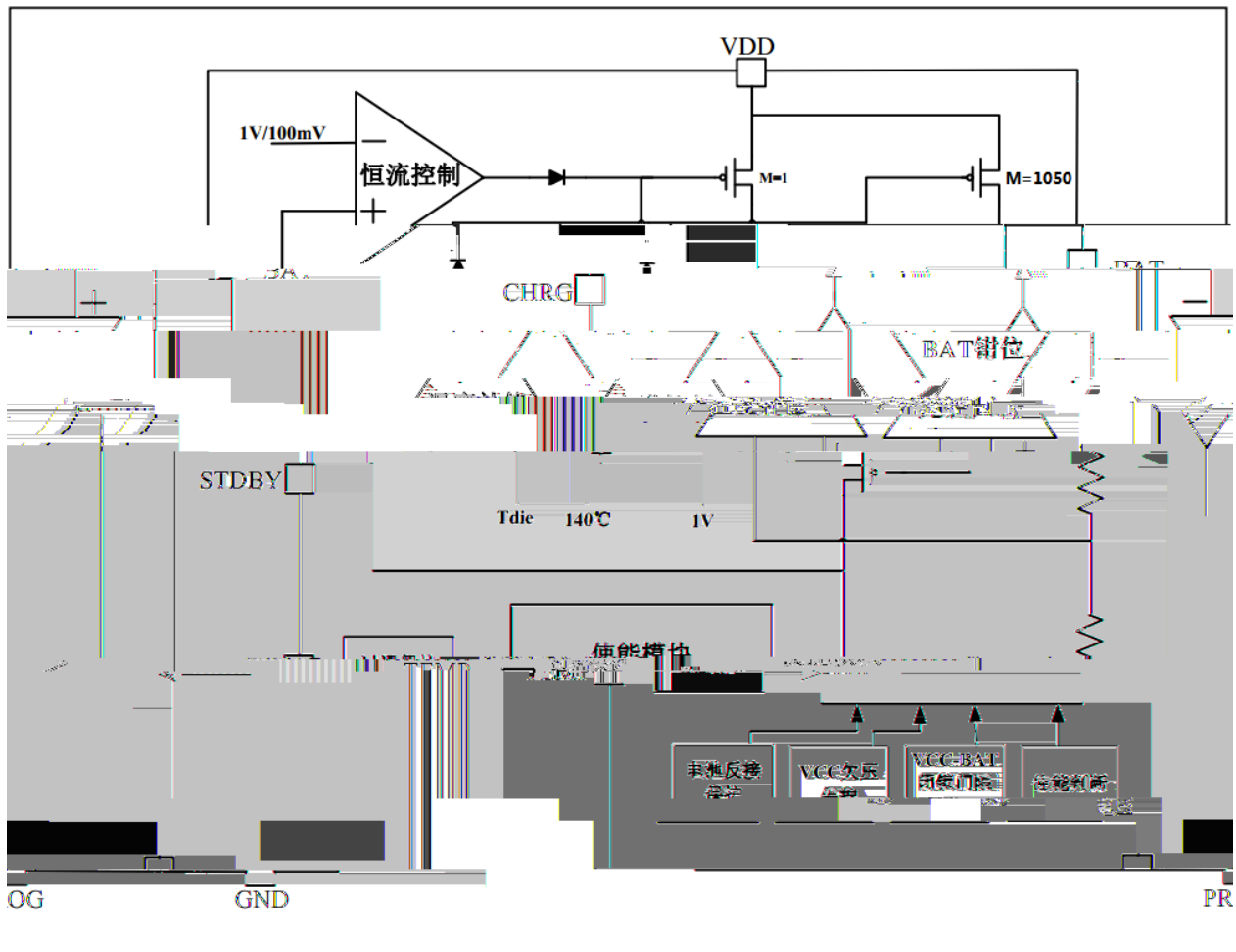
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
<b>Power Supply</b>						
Input Supply Voltage	V <sub>DD</sub>		4.5	5.0	6.0	V
V <sub>DD</sub> Under Voltage Lockout Threshold	V <sub>DD_UV</sub>	V <sub>DD</sub> from Low to High		3.8		V
V <sub>DD</sub> Under voltage Lockout Hysteresis	V <sub>DD_UV_HYS</sub>	V <sub>DD</sub> from High to Low		200		mV
Input Over-Voltage Protection Voltage	V <sub>DD_OVP</sub>		6.3	6.8	7.3	V
Input Over-Voltage Protection Voltage Hysteresis	V <sub>DD_OVP_HYS</sub>			500		mV
Input Power Supply Current	I <sub>CC</sub>	Charging mode (R <sub>PROG</sub> =1K)		240	360	μA
		Standby mode, charging terminated		70	120	μA
		Shutdown mode (R <sub>PROG</sub> not connected, V <sub>CC</sub> <V <sub>BAT</sub> )		50	100	μA
<b>Charging Current Setting</b>						
R <sub>PROG</sub> voltage at Constant Current Mode	V <sub>PROG</sub>	V <sub>DD</sub> =5V;R <sub>PROG</sub> =1K	0.85	1	1.15	V

BAT Pin Current I

/ Electrical Characteristics(Ta=25 )

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
<b>Indicator Pin Status CHRG/STDBY</b>						
CHRG Pin Output Current	I <sub>CHRG</sub>	V <sub>DD</sub> =5V;V <sub>CHRG</sub> =1V	1	2.5	5	mA
STDBY Pin Output Current	I <sub>STDBY</sub>	V <sub>DD</sub> =5V;V <sub>STDBY</sub> =1V	1	2.5	5	mA
<b>Internal Temperature Compensation</b>						
Internal Temperature Compensation	T <sub>OTC</sub>			140		
Overtemperature Detection Threshold	T <sub>OTPH</sub>	K<D G E K:	43%* VDD	45%* VDD		V
Low Temperature Detection Threshold	T <sub>OTPL</sub>	K<D G E K:		80%* VDD	82%* VDD	V

/ Principle block diagram



# BRCL4070ZR-4.2

Rev.B Mar.-2026



DATA SHEET

BRCL4070ZR-4.2 /  
 BRCL4070ZR-4.2 USB AC1.2A ,  
 6.8V

UVLO PROG 1%  
 BAT 2.8V,  
 1/5  
 BAT 2.8V  
 BAT BRCL4070ZR-4.2  
 1/10

$I_{CH} = 1950/R_{PROG}$   $I_{CH}$   
 $R_{PROG}$   $R_{PROG}$  200mA  
 $R_{PROG} = 1950/0.2 = 5.25K$   
 $R_{PROG}$  1%

$R_{PROG}(K)$ I	$I_{CH}(mA)$
1	1950
1.2	875
5	205
10	100

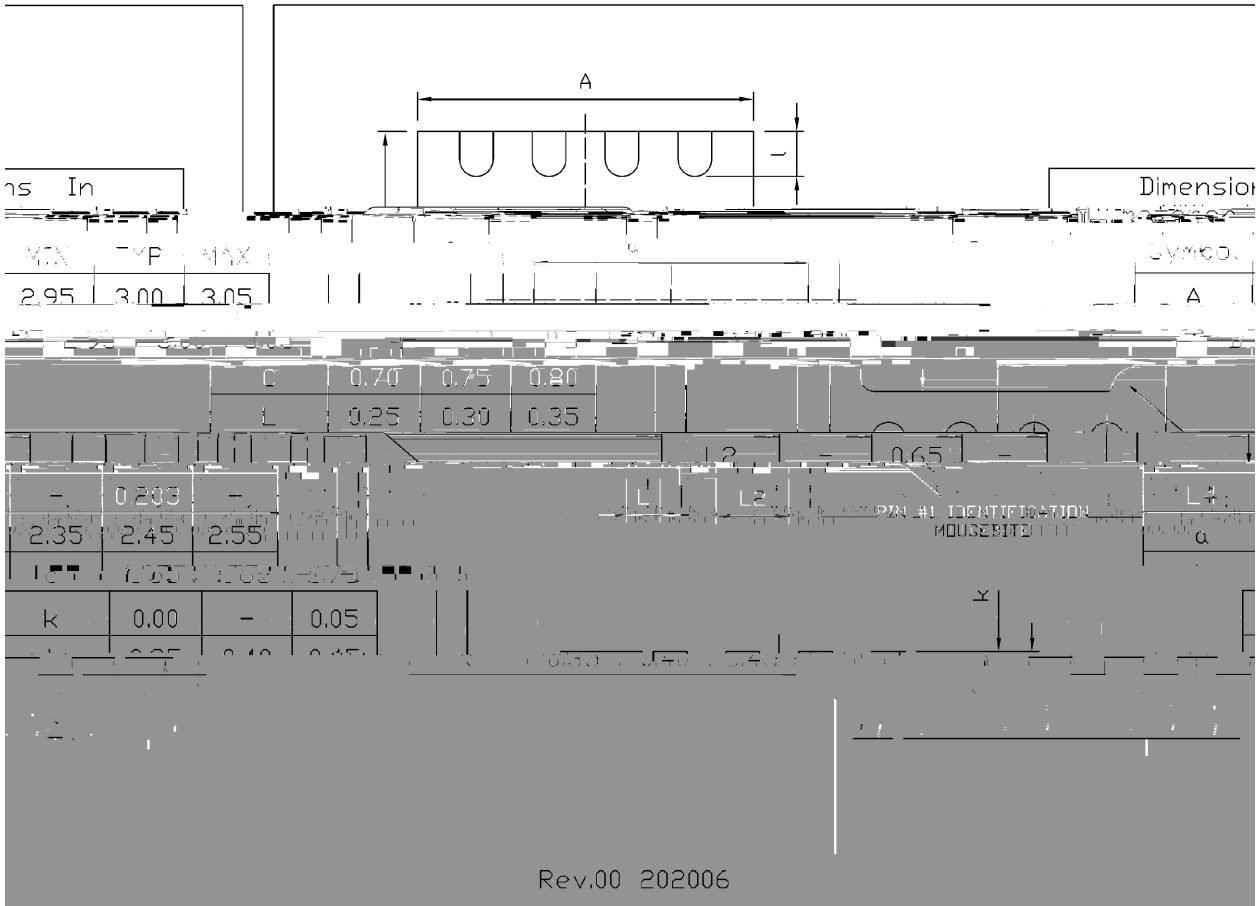
BAT 4.05V 2ms  $T_{RECHARGE}$  80



**/ Package Dimensions**

DFN3X3B-8L

Unit:mm





/ Marking Instructions



9|  
+' 7'  
XX  
!!!!

BRCL4070ZR-4.2	BR/4070-42/****

# BRCL4070ZR-4.2

Rev.B Mar.-2026



DATA SHEET

( ) /


### Note:

- |   |         |           |   |
|---|---------|-----------|---|
| 1 | 150 180 | 60 90sec; | 1.Preheating:150~180 , Time:60~90sec.   |
| 2 | 245±5   | 5±0.5sec; | 2.Peak Temp.:245±5 , Duration:5±0.5sec. |
| 3 | 2 10    | /sec.     | 3. Cooling Speed: 2~10 /sec.            |

### / Resistance to Soldering Heat Test Conditions

260±5                      10±1 sec.                      Temp.:260±5                      Time:10±1 sec

### / Packaging SPEC.

/ REEL

Package Type	Units	Dimension	(unit mm <sup>3</sup> )
--------------	-------	-----------	-------------------------