

5 é / Descriptions

SOT-23 .> // xJ' PNP f â 'ož Silicon PNP Digital Transistor in a SOT-23 Plastic Package.

α a / Features

Ä x ž â Š k • ç è — ‹ k x 5 « ^ y ò - v i k —)í D }ož

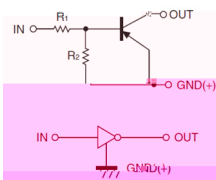
With built-in bias resistors, simplify circuit design, reduce a quantity of parts and manufacturing process, HF Product.

Đ ÷ / Applications

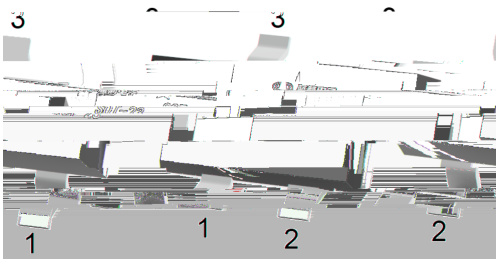
9 • ¼o• μ è Y C Ü è ož

Switching applications, interface circuit and driver circuit applications.

Ã W] Ô . / Equivalent Circuit



• Ű - æ / Pinning



PIN1 y IN

PIN 2 y GND

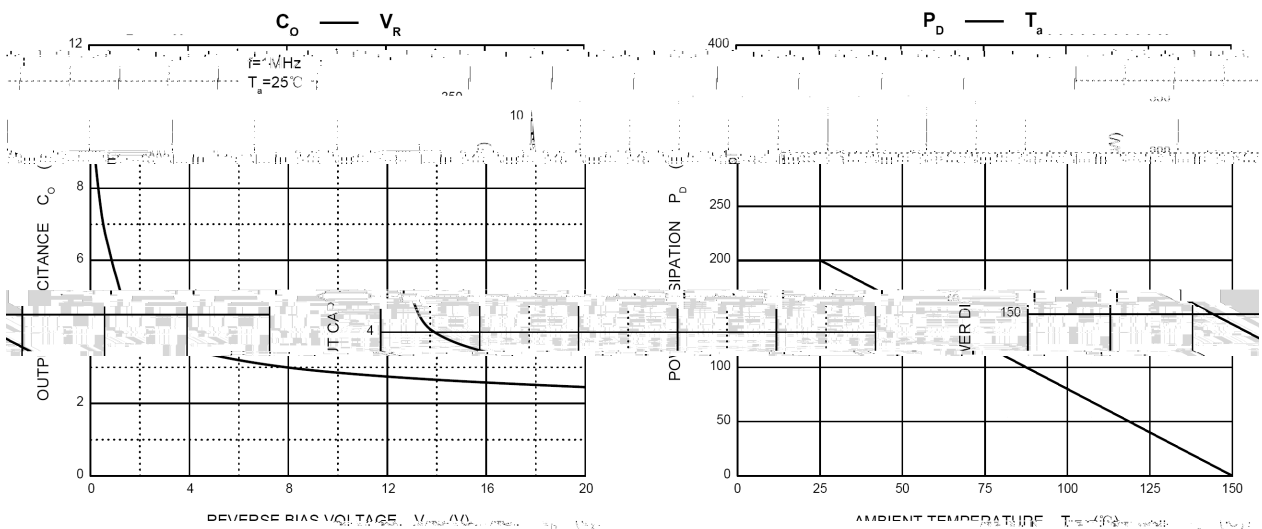
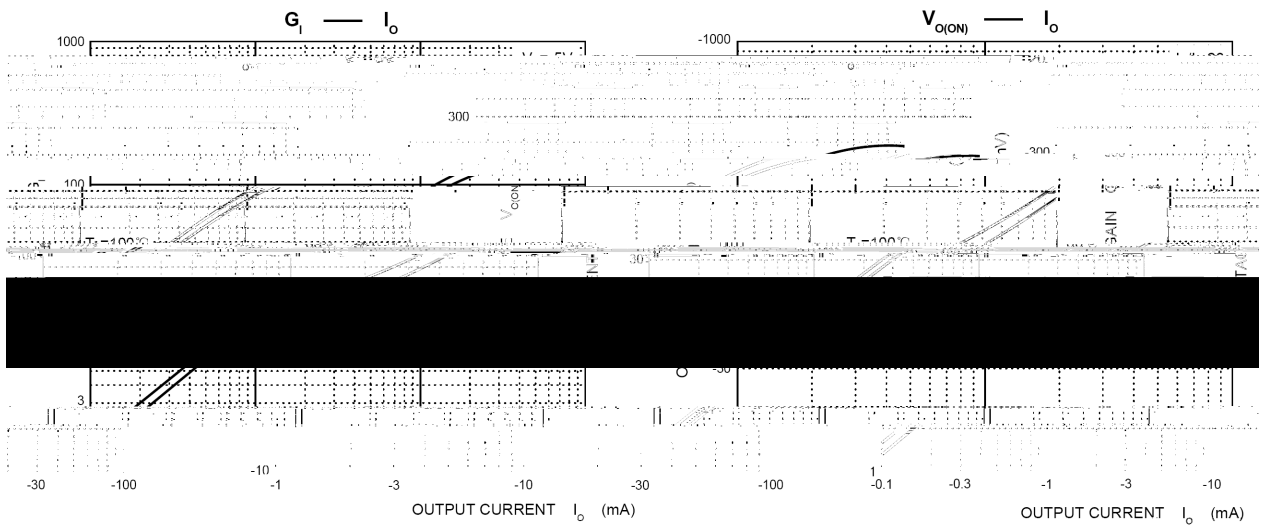
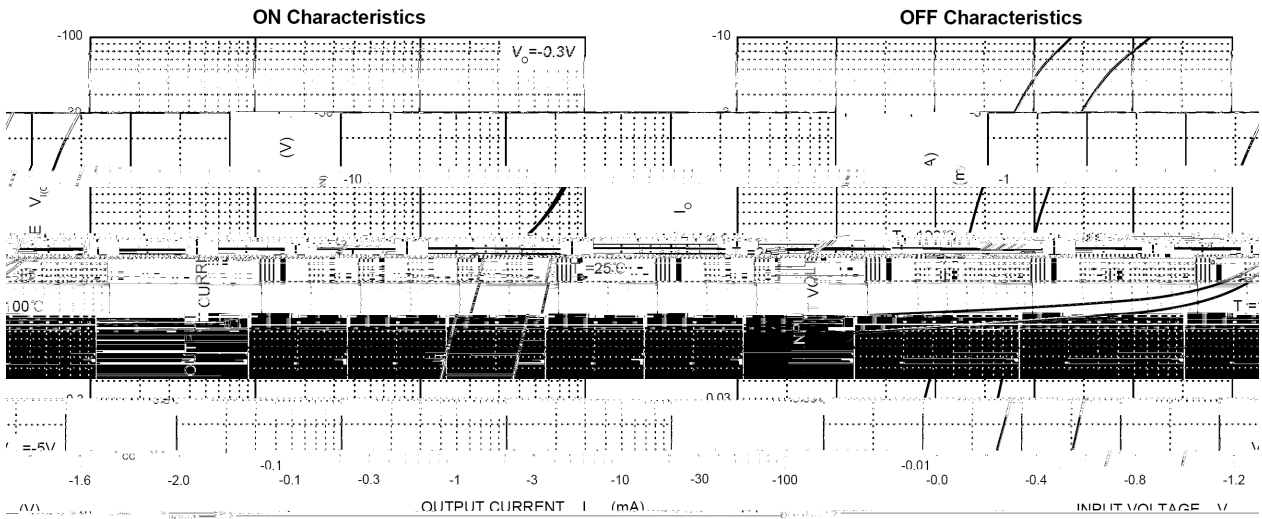
PIN 3 y OUT

Z Ü B , M V / hFE Classifications & Marking

Marking	H14
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$@ f$ Parameter	$\dots Z$ Symbol	$f \succ$ Rating	$\% y$ Unit
Output Voltage	V_{CC}	-50	V
Input Voltage	V_{IN}	10	V
		-40	V
Output Current	I_o	-50	mA
Peak Collector Current	I_{CM}	-100	mA
Power Dissipation	P_D	200	mW
Junction Temperature	T_j	-55 150	

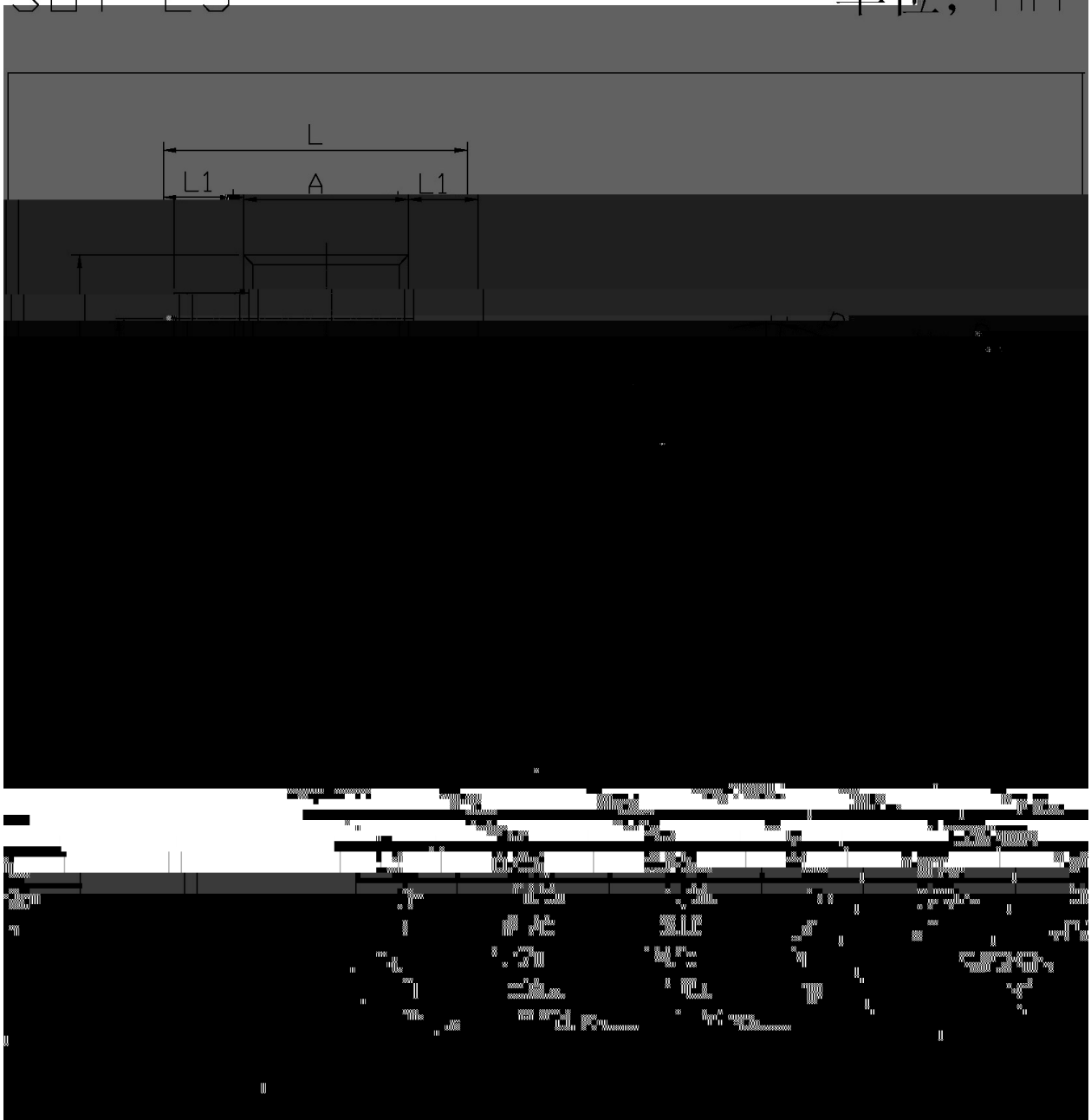
Electrical Characteristic Curve



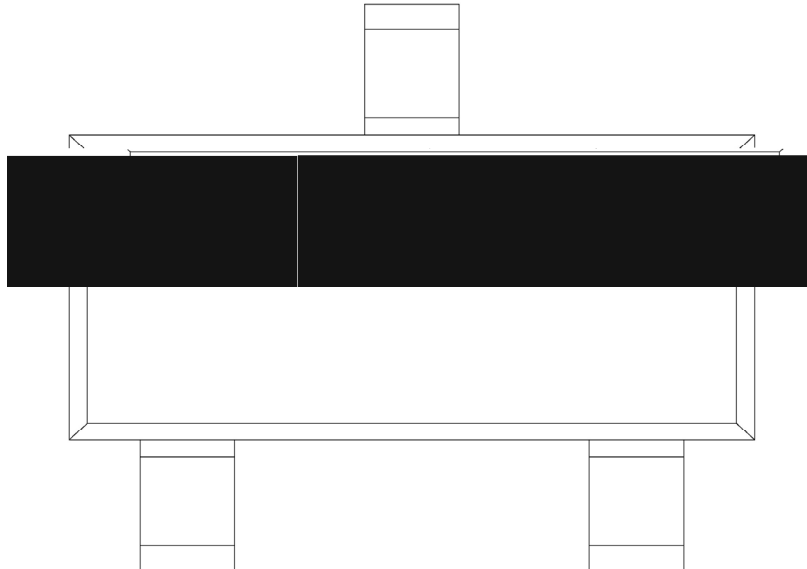
Ø □ =) φ / Package Dimensions

SOT-23

单位: mm



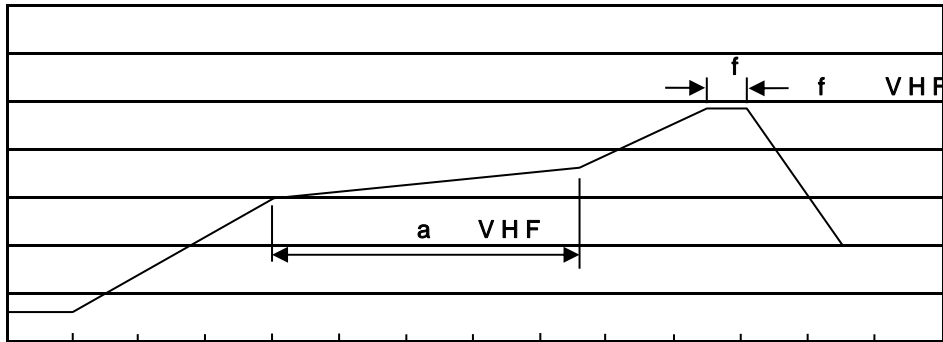
, M y f / Marking Instructions



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. y , [W A
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šWD t...•Žç (x/) / :KSVKXGZ[XK 6XULORK LUX /8 8KLRU] 9URJKXOTM 6

7HPSHUDWXUH



7LPH VHF

^açy

1o•Ä½“† 150½180 - kž• 60½90sec;

2o•Q›“† 245 r5 - kž•4 Ò 5 r0.5sec;

3o•D N ò i Ò 0 , † 2½10 - /sec.

Note:

1.Preheating:150~180 - , Time:60~90sec.

2.Peak Temp.:245 r5 - , Duration:5 r0.5sec.

3. Cooling Speed: 2~10 - /sec.

ÂD /Cã p ~ »] / Resistance to Soldering Heat Test Conditions

“†y 260 r5 -

ž•y 10 r1 sec.

Temp.:260±5

Time:10±1 sec

G P á / Packaging SPEC.

2 & x / REEL

Package Type 7>û ~ E	Units ;>û !H					Dimension ;>û p . (unit /Æmm ³)		
	Units/Reel /--	Reels/Inner Box -- /-	Units/Inner Box /-	Inner Boxes/Outer Box - /!ç	Units/Outer Box /!ç	Reel	Inner Box	Outer Boxç
SOT-23	3,000	10	30,000	6	180,000	7 T×8	180×120×180	390×385×205

„Đ y f / Notices