/ Descriptions

WR0<50P ṂṂ Ŀ QSQ ÆṀ ɥ s Ⅱ ˥ Silicon NPN transistor in a TO-92LM Plastic Package.

/ Features

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High Collector-Base Voltage, high Current Gain Bandwidth Product.

/ Applications

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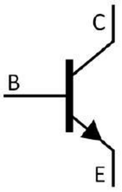
High voltage power amplifier.

/ Equivalent Circuit



/ Pinning

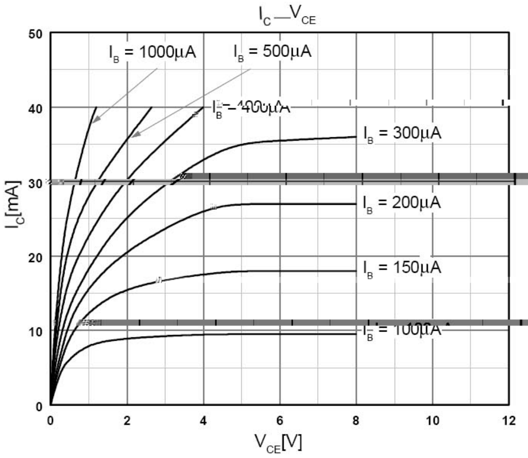
PIN1 χ Base PIN 2 χ Collector PIN 3 χ Emitter

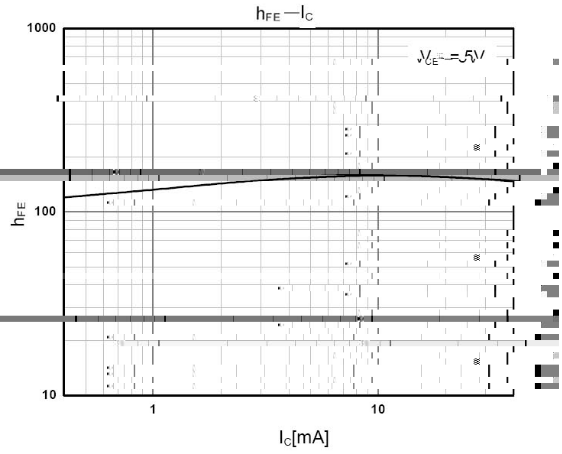
/ h_{FE} Classifications & Marking

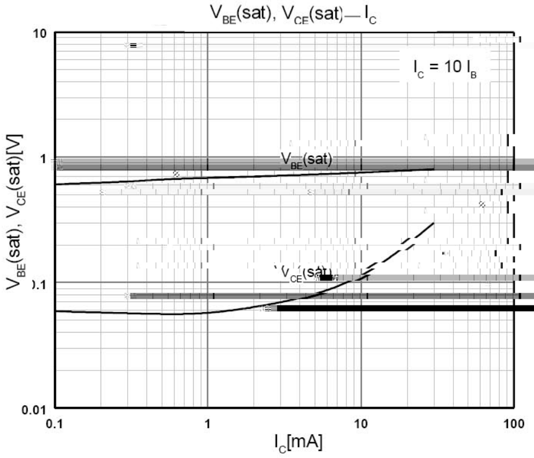
h_{FE} Classifications Symbol	R	O	Y
h_{FE} Range	40 80	70 140	120 240

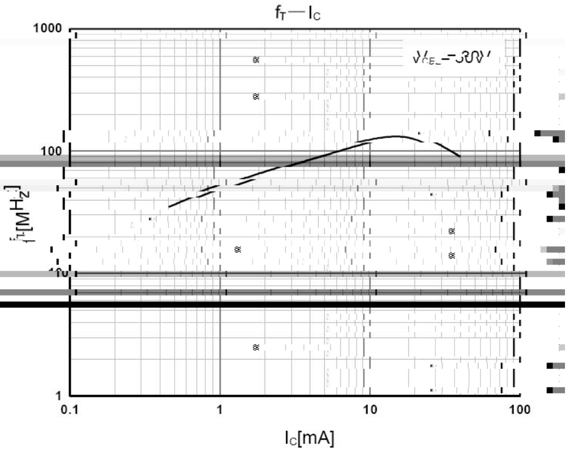
Parameter	Symbol	Rating	Unit
Collector to Base Voltage	V_{CBO}	200	V
Collector to Emitter Voltage	V_{CEO}	150	V
Emitter to Base Voltage	V_{EBO}	5.0	V
Collector Current (DC)	I_C	50	mA
Collector Power Dissipation	P_C	800	mW

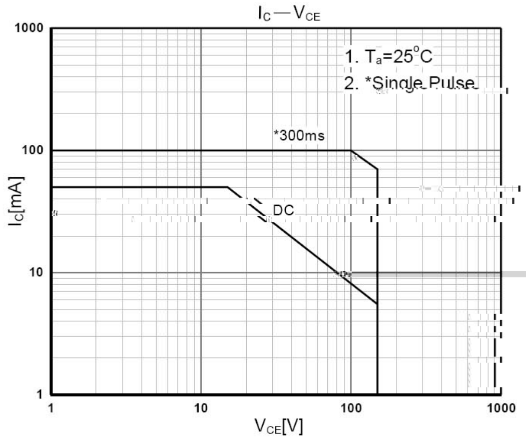
/ Electrical Characteristic Curve

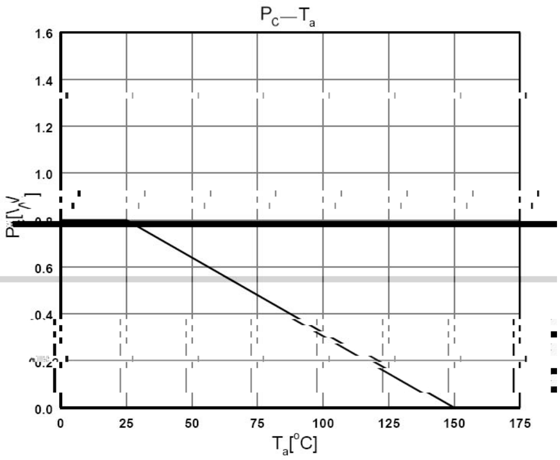






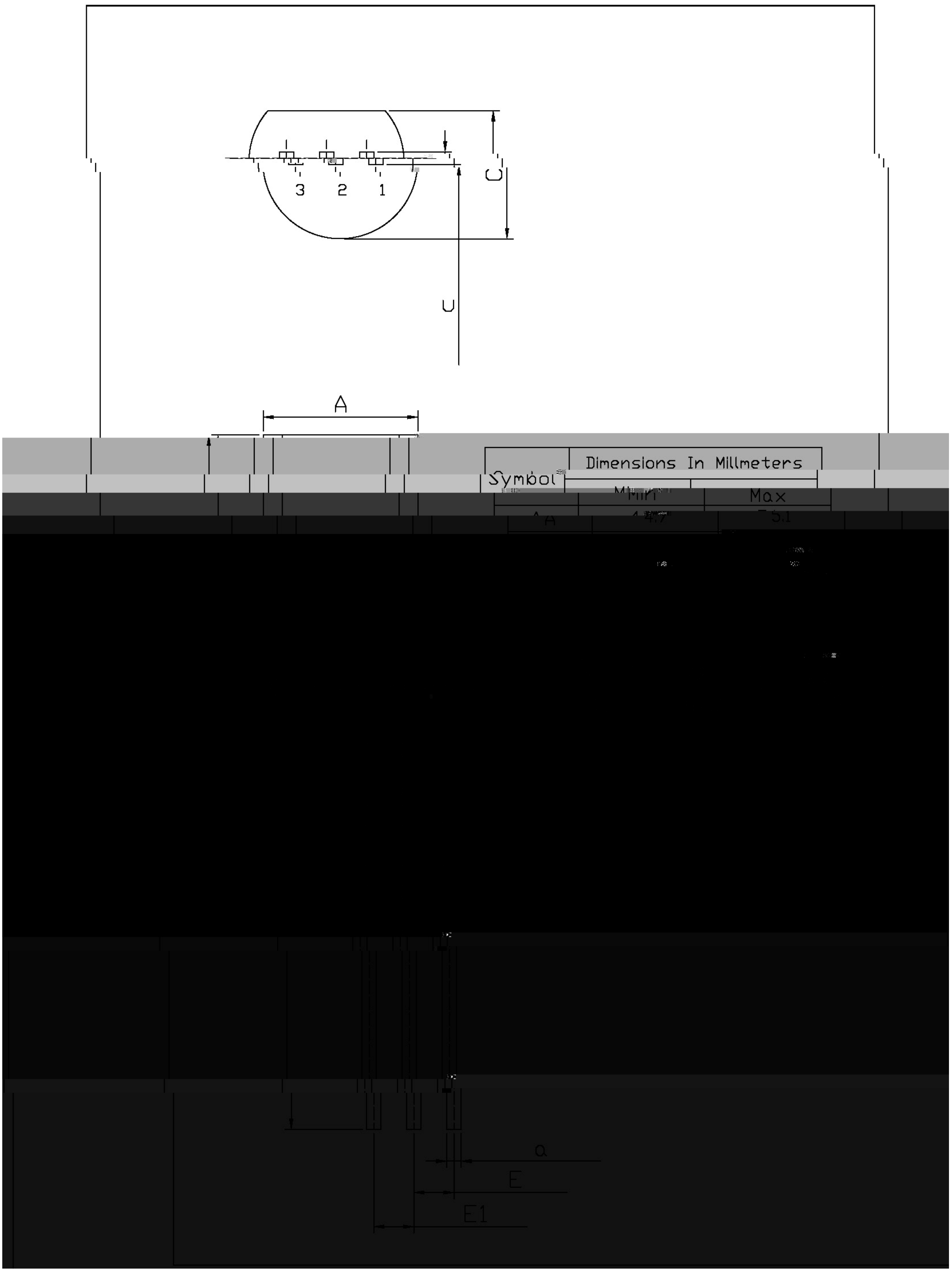






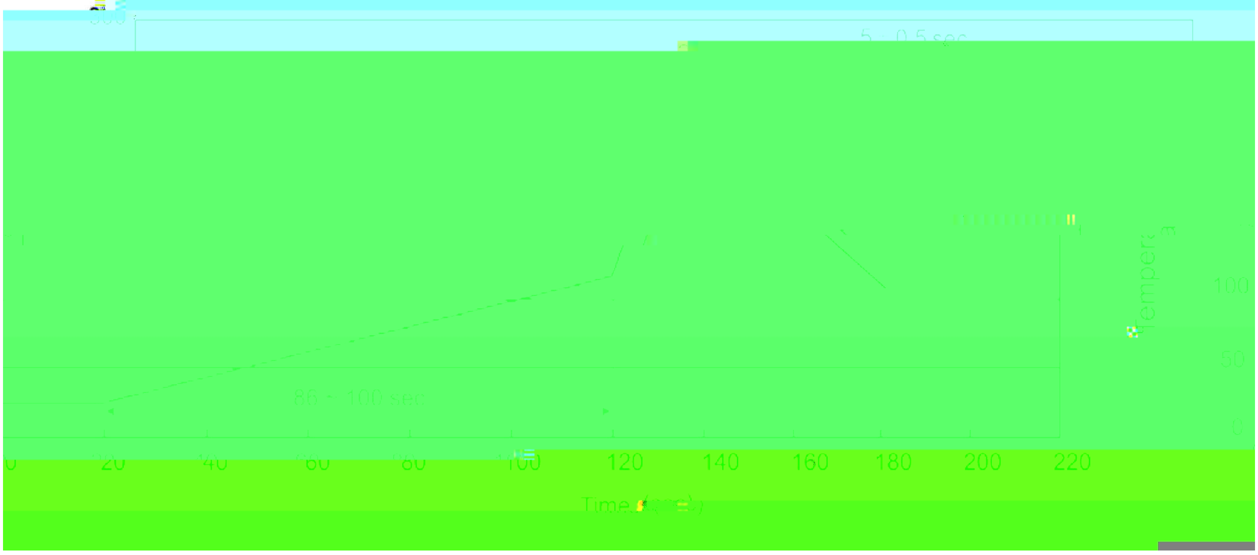
/ Package Dimensions

TO-92LM Unit: mm



Symbol	Dimensions In Millmeters	
	Min	Max
A	4.7	5.1

(　　) / Temperature Profile for Dip Soldering(Pb-Free)



ب ˝ χ	Note:
1 ȴ ♬ □ ẙ 25 Ḱ 150 ɫ ı `H ڗ 60 Ḱ 90sec;	1.Preheating:25~150 ɫ , Time:60~90sec.
2 Ś Ӫ □ ẙ 255 ° 5 ɫ ı `H ڗ ʽY ᛅ ᵯ 5 ° 0.5sec;	2.Peak Temp.:255 ° 5 ɫ , Duration:5 ° 0.5sec.
3 ì ˙ ˥ ˛ ɔ ﻬ ẙ ᵯ 2 Ḱ 10 ɫ /sec.	3. Cooling Speed: 2~10 ɫ /sec.

/ Resistance to Soldering Heat Test Conditions

□ ẙ χ 270 ° 5 ɫ	`H ڗ χ 10 ° 1 sec.	Temp.:270±5℃	Time:10±1 sec

/ Packaging SPEC.

˝Ω ʈ ɣ ʟ / BULK

Package Type 封装形式	Units 包装数量					Dimension 包装尺寸 (unit: mm3)		
	只 袋	袋 盒	只 盒	盒 箱	只 箱	袋	盒	箱

ᛅ Ṿ ɣ ʟ / AMMO

Package Type 封装形式	Units 包装数量					Dimension 包装尺寸 (unit: mm3)	
	只 纸带	纸带 盒	纸带层 盒	盒 箱	只 箱	盒	箱
							小箱 大箱

/ Notices