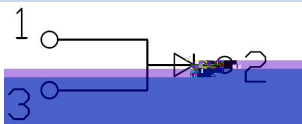


/ Descriptions

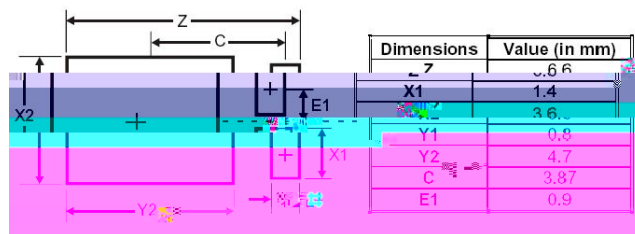
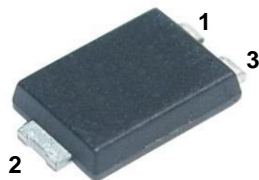
/ Features

/ Applications

/ Equivalent Circuit



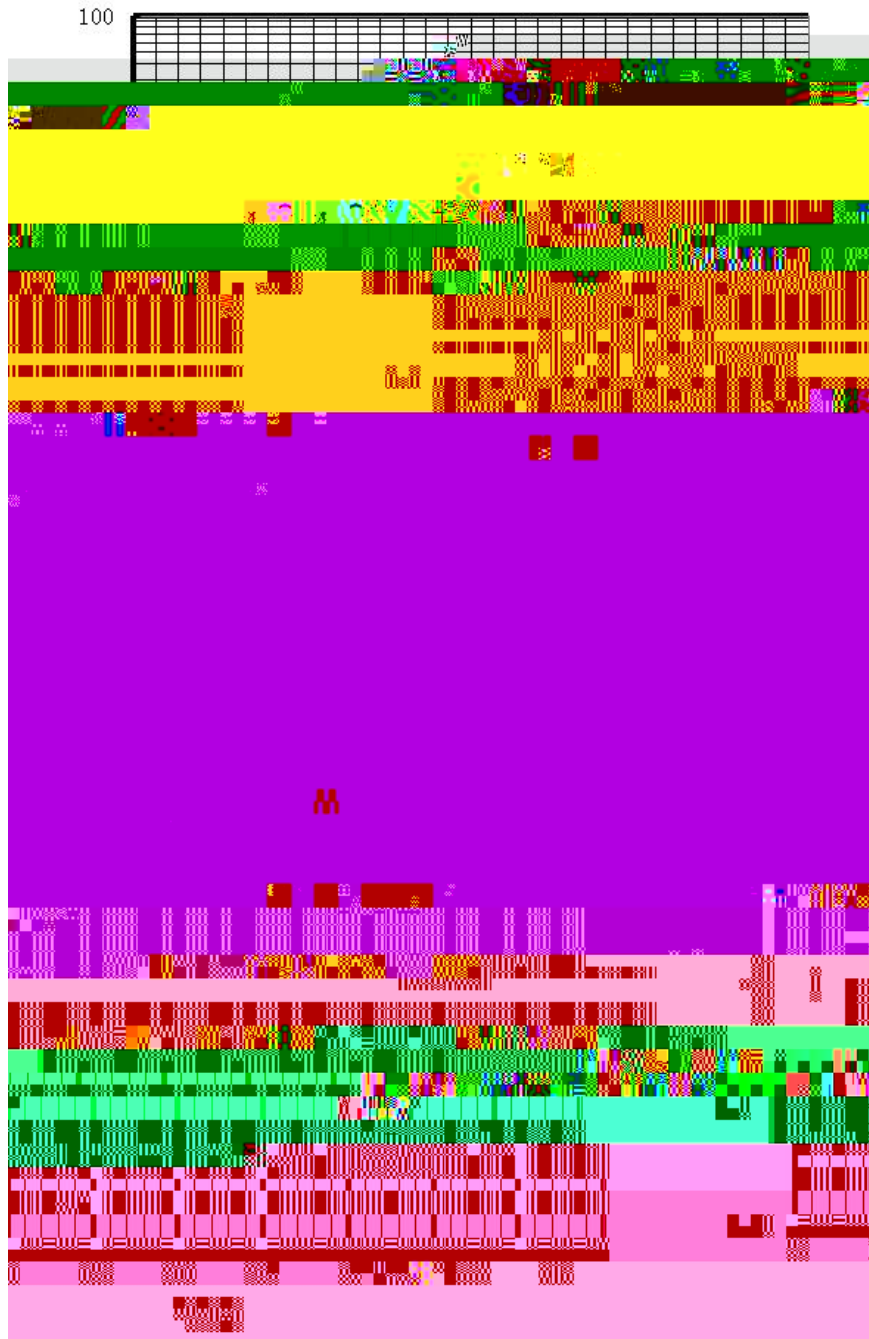
/ Pinning



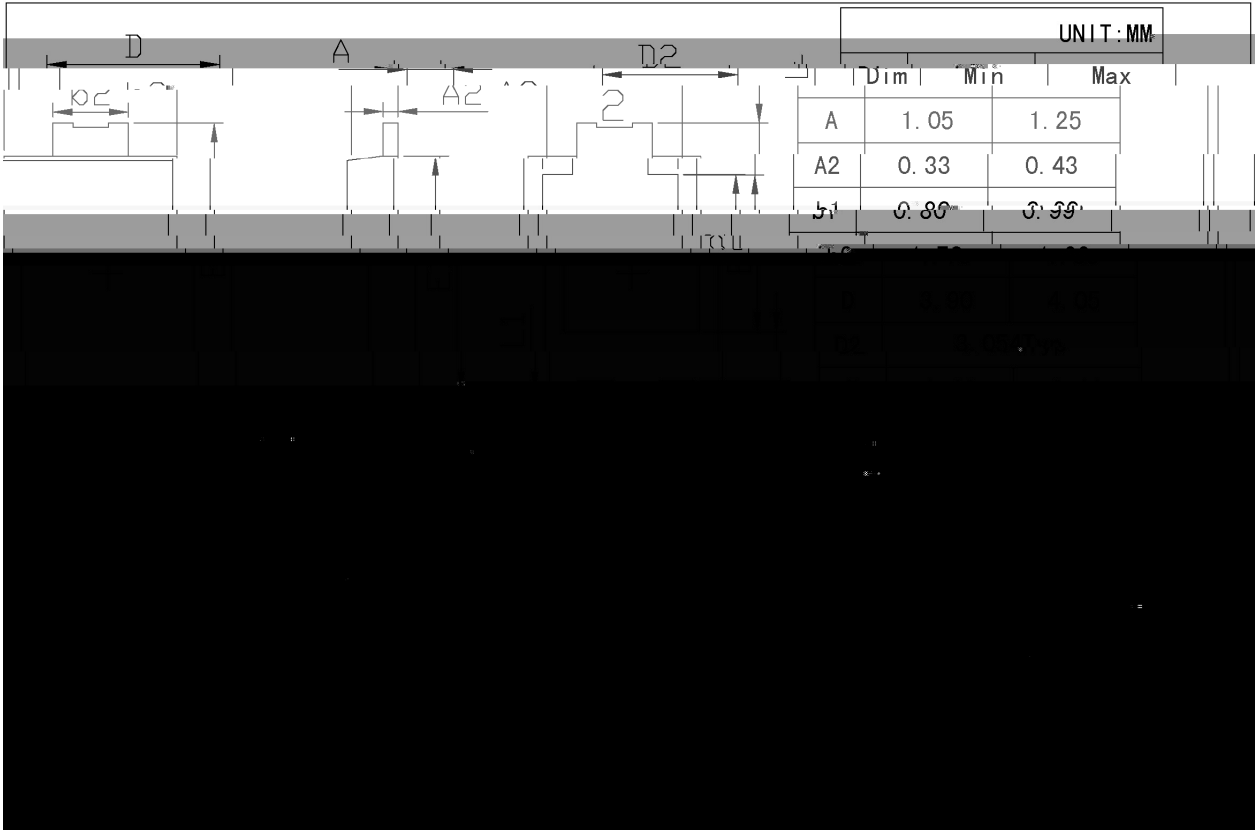
/ h_{FE} Classifications & Marking

/ Electrical Characteristic Curve

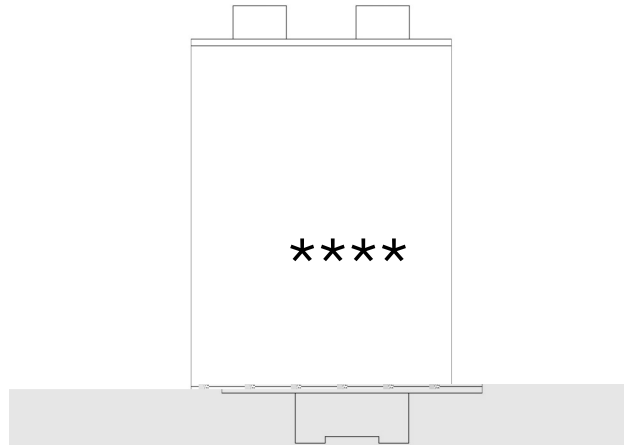
V_F-I_F



/ Package Dimensions



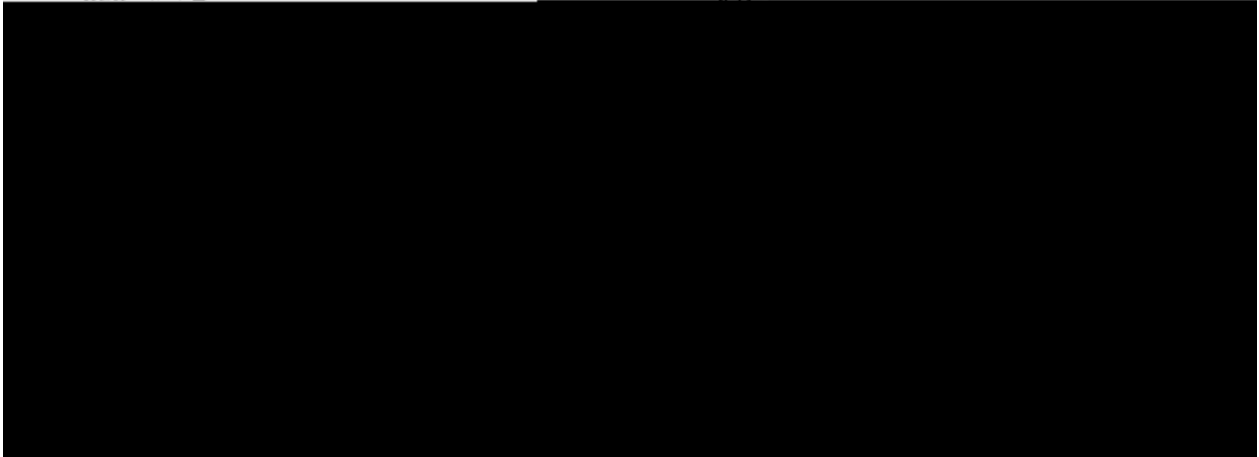
/ Marking Instructions



(' H +,

(' H +,

() / Temperature Profile for IR Reflow Soldering(Pb-Free)



±

±

±

±